Maulana Abul Kalam Azad University of Technology, West Bengal
(Formerly West Bengal University of Technology)
Syllabus for B. Tech in Computer Science & Engineering
(Applicable from the academic session 2018-2019)

Practical

Subject Code : PCC-CS 492	Category: Professional Core course
Subject Name : Computer architecture Lab	Semester : Four
L-T-P : 0-0-4	Credit:2
Pre-Requisites: No-prerequisite	

All laboratory assignments are based on Hardware Description Language (VHDL or Verilog) Simulation.
[Pre-requisite: The hardware based design has been done in the Analog& Digital Electronics laboratory and ComputerOrganisation laboratory]
1. HDL introduction
2. Basic digital logic base programming with HDL
3. 8-bit Addition, Multiplication, Division
4. 8-bit Register design
5. Memory unit design and perform memory operatons.
6. 8-bit simple ALU design
7. 8-bit simple CPU design
8. Interfacing of CPU and Memory